

Requirement Summary:

Module Name	High Level Requirement	Points
Input Module	<ul style="list-style-type: none">• This module should successfully obtain the captured video from the camera and convert it into digital form using an ADC• Besides, this module also sends the OBD II data to the FPGA and control module without errors	10
FPGA and Control Module	<ul style="list-style-type: none">• This module should successfully overlay the data obtained from the OBD II on the video received from the input module	15
Storage Module	<ul style="list-style-type: none">• This module should successfully store the overlaid video without corrupting the file system at a reasonable frame rate	10
Display Module	<ul style="list-style-type: none">• This module should display the overlaid video without any glitches or freezes	10
Power Module	<ul style="list-style-type: none">• This module should supply enough steady power to the circuit	5
	Total	50

Source: Made using 'Automotive Racing Video Data Logger' from Fall 2012.